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PATENT NUMBER

56 852

O.J.P.E.

PATENT DATE

SCANNE

Q.A. Am

APPLICATION NO. 09/965841	CONT/PRIOR	CLASS 327 361	SUBCLASS 767	ART UNIT 2827	EXAMINER DINA
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APPLICANTS

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III

Integrated circuit package including interconnection posts for multiple electrical connections

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL			CROSS REFERENCE(S)						
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION									

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig. 8B	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)			
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		ISSUE FEE	
	_____ (Date)			
			Amount Due	Date Paid
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